

03/23/99
JCS25 U.S. PTO

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

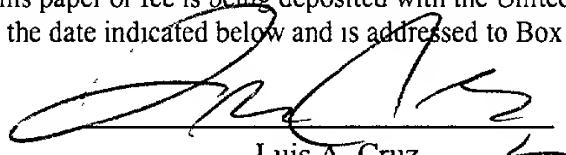
In re Application of: Jung-Hyun Hwang & Woon Na
Serial No.: Not Yet Assigned
Filing Date: HEREWITH
Title: IMAGING APPARATUS AND IMAGE SIGNAL PROCESSING
METHOD PRODUCING WIDE DYNAMIC RANGE
Docket No.: SAMJ-069

A
JC612 U.S. PTO
09/27/3643
03/23/99

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.10

"Express Mail" Mailing Label Number EL263624100US I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated below and is addressed to Box Patent Application, Assistant Commissioner for Patents, Washington, DC 20231.

3/23/99
Date of Deposit


Luis A. Cruz

BOX PATENT APPLICATION
Assistant Commissioner for Patents
Washington, DC 20231

Sir:

TRANSMITTAL LETTER

Enclosed herewith for filing in the above-identified patent application please find the following listed items:

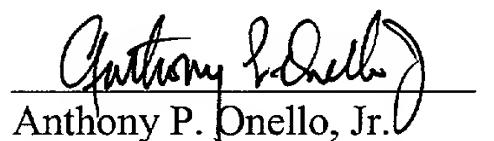
1. New Application Transmittal;
2. New Patent Application;
3. Declaration, Petition and Power of Attorney (original signature);
4. Five (5) Sheets of Formal Drawings;
5. Certified Copy of Priority Document -- Korean Patent Application No. 98-35852;
6. Assignment of Patent Rights (original signature);
7. Form PTO-1595;
8. Check in the amount of \$40.00 to cover requisite fee;
9. Information Disclosure Statement;
10. Form PTO-1449 citing one (1) patent;
11. Copy of cited patent;
12. Check in the amount of \$760.00 to cover application filing fee; and
13. Return Postcard.

In connection with the foregoing matter, please charge any additional fees which may be due, or credit any overpayment, to Deposit Account Number 12-0448. A duplicate copy of this letter is provided for this purpose.

Respectfully submitted,

LAPPIN & KUSMER LLP

Date: March 23, 1999
Two Hundred State Street
Boston, MA 02109
Telephone: (617) 330-1300
Facsimile: (617) 330-1311


Anthony P. Onello, Jr.
Registration Number 38,572
Attorney for Applicants
G:\SAMJ\069\APPLN\TRANS.APP

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

BOX PATENT APPLICATION
Assistant Commissioner for Patents
Washington, D.C. 20231

NEW APPLICATION TRANSMITTAL

Transmitted herewith for filing is the patent application of

Inventor(s): Jung-Hyun Hwang & Woon Na

For (title): IMAGING APPARATUS AND IMAGE
SIGNAL PROCESSING METHOD
PRODUCING WIDE DYNAMIC RANGE

1. Type of Application This new application is for a(n)

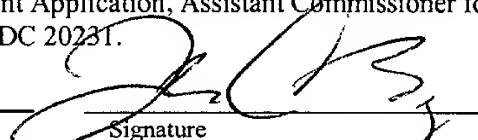
- Original (nonprovisional)
- Design
 - Plant
 - Divisional.
 - Continuation.
 - Continuation-in-part (C-I-P).

2. Benefit of Prior Application(s)

- The new application being transmitted claims the benefit of prior Korean application(s) no. 98-35852.
See item 8 below where benefit of prior application(s) is claimed.

3. Papers Enclosed

- 12 Pages of specification
- 5 Pages of claims
- 1 Page of Abstract
- 5 Sheets of drawings
 - formal
 - informal
- 1 Page of Cover Sheet

CERTIFICATE OF MAILING 37 C.F.R. § 1.10	
<p>"Express Mail" Mailing Label Number <u>EL263624100US</u> I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to Box Patent Application, Assistant Commissioner for Patents, Washington, DC 20231.</p>	
Date: <u>March 23, 1999</u>	 Signature
<u>Luis A. Cruz</u> Print Name	

- The enclosed drawing(s) are photograph(s), and there is also attached a "PETITION TO ACCEPT PHOTOGRAPH(S) AS DRAWING(S)." 37 C.F.R. 1.84(b).

4. Additional papers enclosed

- Preliminary Amendment
- Information Disclosure Statement (37 C.F.R. 1.98)
- Form PTO-1449 (PTO/SB/08A and 08B)
- Copies of cited references
- Declaration of Biological Deposit
- Submission of "Sequence Listing," computer readable copy and/or amendment pertaining thereto for biotechnology invention containing nucleotide and/or amino acid sequence.
- Authorization of Attorney(s) to Accept and Follow Instructions from Representative
- Special Comments
- Other: Return Postcard.

5. Declaration or oath

- Enclosed
 - Unexecuted
 - Executed by
 - inventors
 - legal representative of inventor(s).
37 CFR 1.42 or 1.43.
 - joint inventor or person showing a proprietary interest on behalf of inventor who refused to sign or cannot be reached.
 - This is the petition required by 37 CFR 1.47 and the statement required by 37 CFR 1.47 is also attached. See item 12 below for fee.
- Not Enclosed
- Application is made by a person authorized under 37 C.F.R. 1.41 (c) on behalf of all the above named inventor(s).
 - Showing that the filing is authorized.

6. Language

- English
- Non-English
- The attached translation is a verified translation. 37 C.F.R. 1.52(d).

7. Assignment

- An assignment of the invention to Samsung Electronics Co., Ltd.
- is attached. A separate "COVER SHEET FOR ASSIGNMENT (DOCUMENT) ACCOMPANYING NEW PATENT APPLICATION" or FORM PTO 1595 is also attached.
- will follow.

8. Certified Copy

Certified copy(ies) of application(s)

<u>Republic of Korea</u>	<u>98-35852</u>	<u>1 September 1998</u>
Country	Appln. no.	Filed
<hr/>		
Country	Appln. no.	Filed
<hr/>		

from which priority is claimed

- is (are) attached.
- will follow.

9. Fee Calculation (37 C.F.R. 1.16)

- A. Regular application

CLAIMS AS FILED				
	Number filed	Number Extra	Rate	Basic Fee 37 C.F.R. 1.16(a) \$760.00
Total				
Claims (37 CFR 1.16(c))	20	- 20 =	0	\$ 18.00
Independent				
Claims (37 CFR 1.16(b))	3	- 3 =	0	\$ 78.00
Multiple dependent claim(s), if any (37 CFR 1.16(d))			+	\$260.00

- Amendment cancelling extra claims is enclosed.
- Amendment deleting multiple-dependencies is enclosed.
- Fee for extra claims is not being paid at this time.

Filing Fee Calculation \$ 760.00

- B. Design application

(\$320.00-37 CFR 1.16(f))

Filing Fee Calculation \$ _____

- C. Plant application

(\$530.00-37 CFR 1.16(g))

Filing fee calculation \$ _____

10. Small Entity Statement(s)

- Verified Statement(s) that this is a filing by a small entity under 37 CFR 1.9 and 1.27 is (are) attached.

- Status as a small entity was claimed in prior application _____, filed on _____ from which benefit is being claimed for this application under:

35 U.S.C. 119(e),

120,

121,

365(c),

and which status as a small entity is still proper and desired.

- A copy of the verified statement in the prior application is included.

Filing Fee Calculation (50% of A, B or C above)

\$_____

11. Request for International-Type Search (37 C.F.R. 1.104(d))

- Please prepare an international-type search report for this application at the time when national examination on the merits takes place.

12. Fee Payment Being Made at This Time

- Not Enclosed

- No filing fee is to be paid at this time.

(This and the surcharge required by 37 C.F.R. 1.16(e) can be paid subsequently.)

- Enclosed

Basic filing fee \$ 760.00

Recording assignment
(\$40.00; 37 C.F.R. 1.21(h))
(See attached "COVER SHEET FOR ASSIGNMENT
ACCOMPANYING NEW APPLICATION".) \$ 40.00

Petition fee for filing by other than all the inventors or person on behalf of the inventor where inventor refused to sign or cannot be reached
(\$130.00; 37 C.F.R. 1.47 and 1.17(h)) \$ _____

For processing an application with a specification in a non-English language
(\$130.00; 37 C.F.R. 1.52(d) and 1.17(k)) \$ _____

Processing and retention fee
(\$130.00; 37 C.F.R. 1.53(d) and 1.21(l)) \$ _____

Fee for international-type search report
(\$40.00; 37 C.F.R. 1.21(e)) \$ _____

Total fees enclosed \$ 800.00

13. Method of Payment of Fees

Checks in the amount of \$ 760.00 & 40.00

Charge Account No. 12-0448 in the amount of \$ _____
A duplicate of this transmittal is attached.

14. Authorization to Charge Additional Fees

The Commissioner is hereby authorized to charge the following additional fees during the entire pendency of this application to Account No. 12-0448.

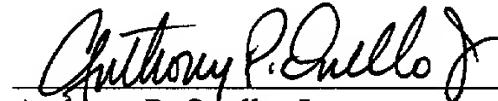
- 37 C.F.R. 1.16(a), (f) or (g) (filing fees)
- 37 C.F.R. 1.16(b), (c) and (d) (presentation of extra claims)
- 37 C.F.R. 1.16(e) (surcharge for filing the basic filing fee and/or declaration on a date later than the filing date of the application)
- 37 C.F.R. 1.17 (application processing fees)
- 37 C.F.R. 1.18 (issue fee at or before mailing of Notice of Allowance, pursuant to 37 C.F.R. 1.311(b))

15. Instructions as to Overpayment

- Credit Account No. 12-0448
- Refund

Respectfully submitted,

LAPPIN & KUSMER LLP



Anthony P. Onello, Jr.
Registration Number 38,572
Attorney for Applicants

Date March 23, 1999

Two Hundred State Street
Boston, MA 02109
617-330-1300
G:\SAMJ\069\APPLN\TRANLTR.WPD

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

of

Jung-Hyun Hwang & Woon Na

for

**IMAGING APPARATUS AND IMAGE SIGNAL PROCESSING
METHOD PRODUCING WIDE DYNAMIC RANGE**

IMAGING APPARATUS AND IMAGE SIGNAL PROCESSING METHOD PRODUCING WIDE DYNAMIC RANGE

BACKGROUND OF THE INVENTION

5 1. Field of the Invention

The present invention relates to an imaging apparatus, and more particularly, to an imaging apparatus and an image signal processing method thereof providing a wide dynamic range.

10 2. Description of the Related Art

The human eye has a dynamic range of approximately 120dB. The dynamic range is a visible range of sight imaging from dark regions to light regions visible to the human eye. However, in general artificial signal processing techniques utilized by an imaging apparatus such as a digital camera adopting a charge coupled device (CCD), the generated images exhibit a range of only approximately 60dB. Ongoing research into signal processing methods has therefore been conducted, with the goal of widening the dynamic range.

15 Also, when an image signal is displayed in the monitor of a camera, the dark regions in a low-luminance image are often displayed darker than the luminescence of an actual imaged dark object. In order to compensate for this phenomenon, a gamma correction method is employed for lightening the extreme dark region by amplifying low-luminance portions of the image according to a predetermined gain.

20 However, in the conventional gamma correction technique, because the signal-to-noise ratio (S/N ratio) in a low-luminance image is often very low, image sensitivity therefore decreases to a extremely low level following amplification. Also, a digitally-quantized value generated from the analog image signal, is converted with a high conversion rate, thereby increasing quantization noise and increasing lumping in the output image. Display of color is likewise adversely affected by the conventional correction process.

Therefore, in the conventional gamma correction technique, dynamic range is limited, low-luminance image sensitivity is decreased, and color display is inaccurate.

SUMMARY OF THE INVENTION

To solve the above problems, it is a first object of the present invention to provide an imaging apparatus having a wide dynamic range.

It is a second object of the present invention to provide an imaging apparatus capable of improving color display in low-luminance regions of the image.

It is a third object of the present invention to provide an image signal processing method for widening dynamic range.

To achieve the first object, a first aspect of the present invention provides an imaging apparatus comprising: first signal processing means for partitioning, or dividing, the level of an analog image signal into a plurality of sections, and for amplifying the analog image signal by a plurality of gains according to each section. At least two of the sections have different corresponding gains. Second signal processing means convert the analog image signal amplified by the different gains into a first digital signal, and non-linearly gamma-corrects the digital signal according to each section.

Preferably, the first signal processing means comprises a first analog-to-digital converter, a gain selector and an amplifier. The first analog-to-digital converter converts the analog image signal into a first digital signal for dividing the analog image signal level into the plurality of sections, and the gain selector selects the corresponding gain from the plurality of different gains according to each section, and outputs the selected gain. Also, the amplifier amplifies the analog image signal with the gain output from the gain selector. Preferably, the plurality of different gains are approximately inversely proportional to the luminance level of the analog image signal.

Preferably, the second signal processing means comprises a second analog-to-digital converter and a gamma corrector. The second analog-to-digital converter converts the image signal amplified by the plurality of different gains into a second digital signal, and the gamma corrector non-linearly gamma-corrects the digital signal according to each section.

5

Preferably, the imaging apparatus further comprises a chrominance controller for controlling a chrominance gain of the output of the second means.

To achieve the second object of the present invention, a second aspect of the present
10 invention provides an imaging apparatus comprising an amplifier, an analog-to-digital converter, a chrominance controller and a digital signal processor. The amplifier amplifies an analog image signal by a predetermined gain, and the analog-to-digital converter converts the amplified analog image signal into a digital signal. Also, the chrominance controller controls chrominance gain of the converted output of the analog-to-digital converter, and the digital signal processor processes
15 the output of the chrominance controller.

Preferably, the chrominance controller comprises a low-pass filter, a chrominance gain selector, a high pass filter, a multiplier, an adder, a divider and a clipper. The low pass filter passes a low-frequency component of the converted output of the second means, to output a
20 luminance signal, and the chrominance gain selector divides the level of the luminance signal into a plurality of sections, and selectively outputs the corresponding gain among the plurality of different gains according to each section. The high pass filter passes a high frequency component of the converted output of the second means, to output a chrominance signal, the multiplier multiplies the chrominance signal by the corresponding gain output from the
25 chrominance gain selector, and the adder adds the output of the multiplier to the luminance signal. Also, the divider divides the output of the adder by a factor, for example 2, and the clipper outputs a digital 0 if the output of the divider is less than 0, the maximum value of the output of the second means if the output of the divider is greater than the maximum value of the

output of the second means, and the output of the divider if the output of the divider is greater than 0 and less than the maximum value of the output of the second means. Preferably, the plurality of different gains are approximately inversely proportional to the level of the luminance signal, or otherwise are not inversely proportional thereto depending on the application.

5

To achieve the third object of the present invention, there is provided an image signal processing method of an imaging apparatus, comprising the steps of: (a) dividing the level of an analog image signal into a plurality of sections, and amplifying the image signal with a plurality of different gains according to each section; and (b) converting the image signal amplified by the plurality of different gains into a first digital signal, and non-linearly gamma-correcting the first digital signal according to each section.

10
15
20
25

Preferably, the step (a) comprises the sub-steps of: (a1) converting the analog image signal into a digital signal to divide into the plurality sections; (a2) selecting the corresponding gain among the plurality of different gains according to each section; and (a3) amplifying the analog image signal with the selected gain. Here, the plurality of different gains may be approximately proportional to the luminance level of the analog image signal.

20

Preferably, the step (b) comprises the sub-steps of converting the image signal amplified with the plurality of different gains into a second digital signal, and non-linearly gamma-correcting the second digital signal according to each section.

25

In order to improve color display in a low-luminance region, the image signal processing method further comprises the step of (c) controlling a chrominance gain of the gamma-corrected digital signal.

Preferably, the step (c) comprises the sub-steps of: (c1) passing a low-frequency component of the gamma-corrected digital signal, to output a luminance signal; (c2) dividing the level of the luminance signal into a plurality of second sections, and selecting the corresponding gain among the plurality of different second gains according to each section; (c3) passing a high-frequency component of the non-linearly gamma-corrected digital signal, to output a chrominance signal; (c4) multiplying the chrominance signal by the selected second gain; (c5) adding the result of the sub-step (c4) to the luminance signal; (c6) dividing the result of the sub-step (c5) by 2; and (c7) clipping to 0 if the result of the sub-step (c6) is less than 0, and to the maximum value of the gamma-corrected digital signal if the result of the sub-step (c6) is greater than the maximum value of the gamma-corrected digital signal, and outputting the result. Here, the plurality of different second gains may be approximately inversely proportional to the level of the luminance signal or may be not inversely proportional thereto if required.

Thus, in the imaging apparatus and the image signal processing method according to the present invention, before gamma correction, the level of a low-luminance region of an analog image signal is amplified with a gain higher than in the level of a high-luminance region. Accordingly, the S/N ratio in the low-luminance region of the image is increased, so that the sensitivity of the image is improved after the gamma correction. Also, a quantized value for the analog image signal, i.e., a digital value, is non-linearly converted, i.e., non-linearly gamma-corrected, to reduce quantization noise, thereby preventing lumping of an output image to a predetermined level. That is, in the imaging apparatus and the image signal processing method thereof according to the present invention, the dynamic range is wide, and a color display characteristic is improved in a low-luminance region.

BRIEF DESCRIPTION OF THE DRAWINGS

The foregoing and other objects, features and advantages of the invention will be apparent from the more particular description of preferred embodiments of the invention, as illustrated in the accompanying drawings in which like reference characters refer to the same parts throughout the different views. The drawings are not necessarily to scale, emphasis instead being placed upon illustrating the principles of the invention.

FIG. 1 is a block diagram of an imaging apparatus according to a preferred embodiment of the present invention.

FIG. 2 is a graph illustrating the gain characteristics of the gain selector of FIG. 1, in accordance with the present invention.

FIG. 3 is a graph illustrating the input-output correlation of the amplifier shown in FIG. 1 in accordance with the present invention.

FIG. 4 is a graph illustrating the characteristics of the gamma corrector shown in FIG. 1 in accordance with the present invention.

FIG. 5 is a circuit diagram of the chrominance controller shown in FIG. 1 in accordance with the present invention.

FIG. 6 is a graph illustrating the characteristics of the chrominance gain selector shown in FIG. 5 in accordance with the present invention.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

Referring to FIG. 1, an imaging apparatus according to a preferred embodiment of the present invention includes a charge coupled device (CCD) 103, a thermal noise remover 105, a first signal processing means 107, a second signal processing means 109, a chrominance controller 111, a digital camera processor 113, a microcomputer 115, and a digital-to-analog converter (DAC) 117. The imaging apparatus operates by an image signal processing method according to the present invention.

The CCD 103 receives an optical image signal via a lens 101 and converts the received image signal into an electrical analog image signal. The thermal noise remover 105 removes thermal noise from the electrical analog image signal output from the CCD 103. In general, the thermal noise remover 105 may comprise a correlated double sampler.

The first signal processing means 107 partitions the voltage level of the analog image signal passed through the thermal noise remover 105 into a plurality of sections, and amplifies the analog image signal with a plurality of different gains which are predetermined according to the sections (see FIGs. 2 and 3, discussed below). Here, the plurality of gains are provided by the microcomputer 115, and are inversely proportional to the lightness of the analog image signal, that is, the level of the luminance. In this manner, low-luminance regions are amplified by a greater gain than high-luminance regions.

In detail, the first signal processing means 107 includes a first analog-to-digital (ADC) 107a, a gain selector 107b and an amplifier 107c. The first ADC 107a converts the analog image signal into a digital image signal, dividing the image signal into the plurality of sections. The gain selector 107b selects the corresponding gain according to each section. The amplifier 107c amplifies the analog image signal with the gain output from the gain selector 107b.

The second signal processing means 109 converts the analog signal amplified with different gains, that is, the output of the amplifier 107c, into a digital signal, and non-linearly gamma-corrects the digital signal according to each section (see FIG. 3, discussed below).

5 In detail, the second signal processing means 109 includes a second ADC 109a and a gamma corrector 109b. The second ADC 109a converts the output of the amplifier 107c into a digital signal. The gamma corrector 109b non-linearly gamma-corrects the digital signal according to each section in consideration of the section-dependent gain of the analog image signal.

10
15 The optimal chrominance controller 111 controls the chrominance gain of the output of the gamma corrector 109b, in order to display improved color in low-illumination regions. In particular, the chrominance controller 111 may be positioned between an ADC and a digital camera processor of a general camera system adopting a CCD, in order to display improved color in low-illumination regions.

20
25 The digital camera processor 113 digitally processes the output of the chrominance controller 111. The DAC 117 converts the output of the digital cameral processor 113 into an analog image signal, and outputs the converted result into a display. The microcomputer 115 calculates the plurality of gain levels from the output of the digital camera processor 113 such that the mean of the luminance distribution in an image is maintained at a predetermined value, and provides the calculated gains to the gain selector 107b, as described above.

FIG. 2 is a graph showing the characteristics of the gain selector 107b shown in FIG. 1.

Here, the horizontal axis represents an input image signal, that is, the voltage level (in mV) of the analog image signal, and the vertical axis represents a corresponding gain for each voltage level.

Referring to FIG. 2, the voltage level of the analog image signal is divided into four sections. If required, the level of the analog image signal may be divided into more than, or less than, four sections. It is optimal that the voltage level of the analog image signal is divided as many sections as possible. However, preferably the level of the analog image signal is classified
5 into the several sections according to the signal processing characteristics of the application. Here, section SV1 pertains to voltages between level 0 and level V1 of the analog image signal, section SV2 covers level V1 to level V2, section SV3 refers to level V2 to level V3, and section SV_{max} covers level V3 to level V_{max}.

10 As described above, in order to amplify the level of the analog image signal in low-luminance regions according to a gain greater than that for high-illumination regions, four gains g₁, g₂, g₃ and g_{max}, which are inversely proportional to the illuminance level of the analog image signal, are provided. Here, the gains g₁, g₂, g₃ and g_{max} are provided to be the maximum
15 within the range in which the output of the amplifier does not exceed the maximum permissible output of the amplifier. In the case shown in FIG. 2, the gain g₁ for section SV1 is 800%, the gain g₂ for section SV2 is 400%, the gain g₃ for section SV3 is 200%, and the gain g_{max} for section SV_{max} is 100%. In the example given, preferably, the level V1 of the analog image signal is 1/8 of the level V_{max} of the analog image signal, the level V2 of the analog image signal is 1/4, and the level V3 of the analog image signal is 1/2 of the level V_{max} of the analog image signal.

20 FIG. 3 is a graph showing an input-output correlation of the amplifier 107c shown in FIG. 1. The outputs of the amplifier 107c are obtained using the four gains g₁, g₂, g₃ and g_{max} shown in FIG. 2. Here, the horizontal axis represents an input image signal, that is, the level of the analog image signal, and the vertical axis represents the output of the amplifier.

Referring to FIG. 3, in the section SV1 corresponding to a low-luminance region of the analog image signal, variance in the output of the amplifier 107c is the greatest. As the luminance increases, that is, from the section SV2 to the section SV_{max}, change in output of the amplifier 107c gradually decreases.

5

FIG. 4 is a graph illustrating the characteristics of the gamma corrector 109b shown in FIG. 1.

Referring to FIG. 4, the gamma corrector 109b shows a non-linear output characteristic according to sections SV1, SV2, SV3 and SV_{max}. That is, the gamma corrector 109b non-linearly gamma-corrects the output of the second ADC 109a according to each section, in consideration of the section-dependent gain, and outputs the gamma-corrected result.

10
15 FIG. 5 is a circuit diagram of a preferred embodiment of the chrominance controller 111 shown in FIG. 1.

20
25 Referring to FIG. 5, the chrominance controller includes a low pass filter 501, a chrominance gain selector 503, a high pass filter 505, a multiplier 507, an adder 509, a divider 511 and a clipper 513.

The low pass filter 501 passes only the low-frequency component of the output of the gamma corrector 109b (see FIG. 1), that is, low-frequency component of a digital input image signal S1/S2, and outputs a luminance signal L. The low pass filter 501 includes a delay 501a for delaying the digital input image signal S1/S2 by, for example 1 clock cycle, and an adder 501b for adding the output of the delay 501a to the digital input image signal S1/S2, to output the luminance signal L.

The chrominance gain selector 503 divides the level of the luminance signal L into a plurality of sections, selects the corresponding gain among the different gains according to each section, and outputs the gain. In general, the plurality of gains are preferably approximately inversely proportional to the level of the luminance signal. In alternative embodiments, however, 5 the relationship need not be inversely proportional.

The high pass filter 505 passes only a high-frequency component of the digital input image signal S1/S2, and outputs a chrominance signal C. The high pass filter 505 includes a delay 505a for delaying the digital input image signal S1/S2 by, for example 1 clock cycle, and a 10 subtractor 505b for subtracting the output of the delay 505a from the digital input image signal S1/S2 and outputting the chrominance signal C.

The multiplier 507 multiplies the chrominance signal C by the gain output from the chrominance gain selector 503. Adder 509 adds the output of the multiplier 507 to the luminance signal L. The divider 511 divides the output of the adder 509, for example by 2. The clipper 513 15 outputs 0 when the output of the divider 511 is less than 0, the maximum value of the digital input image signal S1/S2 when the output of the divider 511 is greater than the digital input image signal S1/S2. Also, the clipper 513 outputs the output of the divider 511 when the output of the divider 511 is greater than 0 and less than the maximum value of the digital input image 20 signal S1/S2.

FIG. 6 is a graph showing the characteristics of the chrominance gain selector 503 shown in FIG. 5. Here, the horizontal axis represents the level of the luminance signal L, and the vertical axis represents again to be applied to multiplier 507.

Referring to FIG. 6, the level of the luminance signal L is divided into six sections SL1, SL2, SL3, SL4, SL5 and SL_{max} , and different gains G1, G2, G3, G4, G5 and G_{max} are provided according to each section. In particular, in order to display an improved color in a low-luminance region, the gains G1, G2, G3, G4, G5 and G_{max} , which are inversely proportional to the level of the luminance signal L as shown in FIG. 6, are provided. However, as explained above, the gains G1, G2, G3, G4, G5 and G_{max} need not be inversely proportional to luminance signal level.

Here, section SL1 pertains to a voltage level between level 0 and level L_1 of the luminance signal L, section SL2 covers level L_1 to level L_2 , section SL3 covers level L_2 to level L_3 , section SL4 covers level L_3 to level L_4 , section SL5 covers level L_4 to level L_5 , and section SL_{max} covers level L_5 to level L_{max} of the luminance signal L.

As described above, in the imaging apparatus and the image signal processing method according to the present invention, the level of an analog signal low-luminance regions of the analog input signal are amplified by a gain greater than the gains applied to the high-luminance regions before gamma correction, allowing for increased signal-to-noise (S/N) ratio in the low-luminance regions of the image, and improving image sensitivity following gamma correction. Also, the digitally-quantized value for the analog image signal is non-linearly gamma-corrected according to the luminance level, so that quantization noise decreases, thereby mitigating or preventing lumping of the output image. In this manner, the imaging apparatus and the image signal processing method according to the present invention provide a wide dynamic range, and improved color display in low-luminance regions of the image is achieved.

While this invention has been particularly shown and described with references to preferred embodiments thereof, it will be understood by those skilled in the art that various changes in form and details may be made therein without departing from the spirit and scope of the invention as defined by the appended claims.

CLAIMS

What is claimed is:

- 5 1. An imaging apparatus comprising:

first signal processing means for partitioning the level of an analog image signal into a plurality of sections, and for amplifying the analog image signal by a plurality of gains according to each section, at least two of the sections having different corresponding gains; and

10 second signal processing means for converting the analog image signal amplified by the different gains into a first digital signal, and for non-linearly gamma-correcting the first digital signal according to each section.

- 15 2. The imaging apparatus of claim 1, wherein the first signal processing means comprises:

an analog-to-digital converter for converting the analog image signal into a first digital signal for partitioning the analog image signal level into the plurality of sections;

a gain selector for selecting the corresponding gain from the plurality of different gains according to each section, and for outputting the selected gain; and

20 an amplifier for amplifying the analog image signal by the gain output from the gain selector.

- 25 3. The imaging apparatus of claim 2, wherein the plurality of different gains are provided by a microcomputer.

4. The imaging apparatus of claim 2, wherein the plurality of different gains are approximately inversely proportional to the luminance level of the analog image signal.

5. The imaging apparatus of claim 1, wherein the second signal processing means comprises:

an analog-to-digital converter for converting the image signal amplified by the plurality of different gains into a second digital signal; and

5 a gamma corrector for non-linearly gamma-correcting the second digital signal according to each section.

10 6. The imaging apparatus of claim 1, further comprising a chrominance controller for controlling chrominance gain of the non-linearly gamma corrected digital output of the second signal processing means.

15 7. The imaging apparatus of claim 6, wherein the chrominance controller comprises:

a low pass filter for passing a low-frequency component of the non-linearly gamma corrected digital output of the second means, to output a luminance signal;

20 a chrominance gain selector for partitioning the level of the luminance signal into a plurality of second sections, and for selectively outputting a corresponding second gain among a plurality of different second gains according to each section;

a high pass filter for passing a high frequency component of the non-linearly gamma corrected digital output of the second means, to output a chrominance signal;

25 a multiplier for multiplying the chrominance signal by the corresponding second gain from the chrominance gain selector;

an adder for adding the output of the multiplier to the luminance signal;

a divider for dividing the output of the adder by approximately 2; and

25 a clipper for outputting a digital 0 if the output of the divider is less than 0, and for outputting the maximum value of the output of the second signal processing means if the output of the divider is greater than the maximum value of the output of the second signal processing means.

8. The imaging apparatus of claim 7, wherein the plurality of different second gains are approximately inversely proportional to the level of the luminance signal.

9. An imaging apparatus comprising:

5 an amplifier for amplifying an analog image signal by a predetermined gain;
an analog-to-digital converter for converting the amplified analog image signal into a digital signal;
a chrominance controller for controlling chrominance gain of the converted output of the analog-to-digital converter; and
10 a digital signal processor for processing the output of the chrominance controller.

10. The imaging apparatus of claim 9, wherein the chrominance controller comprises:

15 a low pass filter for passing a low-frequency component of the converted output of the analog-to-digital converter, to output a luminance signal;

a chrominance gain selector for dividing the level of the luminance signal into a plurality of sections, and for selectively outputting a corresponding gain among the plurality of different gains according to each section;

20 a high pass filter for passing a high frequency component of the converted output of the analog-to-digital converter, to output a chrominance signal;

25 a multiplier for multiplying the chrominance signal by the corresponding gain output from the chrominance gain selector;

an adder for adding the output of the multiplier to the luminance signal;

a divider for dividing the output of the adder by a factor; and

25 a clipper for outputting a digital 0 if the output of the divider is less than 0, and for outputting the maximum value of the output of the analog-to-digital converter if the output of the divider is greater than the maximum value of the output of the analog-to-digital converter.

11. The imaging apparatus of claim 10, wherein the plurality of different gains are approximately inversely proportional to the level of the luminance signal.

12. The imaging apparatus of claim 10, wherein the factor is approximately 2.

5

13. An image signal processing method comprising:
partitioning the level of an analog image signal into a plurality of sections, and
amplifying the image signal by a plurality of different gains according to each section, at
least two of the sections having different corresponding gains; and

10
converting the image signal amplified by the plurality of different gains into a first
digital signal, and non-linearly gamma-correcting the first digital signal according to each
section.

14. The method of claim 13, wherein partitioning comprises:

15
converting the analog image signal into a first digital signal for partitioning the
analog image signal level into the plurality of sections:

selecting the corresponding gain among the plurality of different gains according
to each section; and

amplifying the analog image signal by the selected gain.

20

15. The method of claim 14, wherein the plurality of different gains are provided by a
microcomputer.

25

16. The method of claim 14, wherein the plurality of different gains are
approximately inversely proportional to the luminance level of the analog image signal.

17. The method of claim 13, wherein converting comprises:
converting the image signal amplified by the plurality of different gains into a
second digital signal; and
non-linearly gamma-correcting the second digital signal according to each section.

5

18. The method of claim 13, further comprising controlling chrominance gain of the
non-linearly gamma-corrected second digital signal.

10

19. The method of claim 17, wherein controlling comprises:
passing a low-frequency component of the non-linearly gamma-corrected digital
signal, to output a luminance signal;

15 partitioning the level of the luminance signal into a plurality of second sections,
and selecting a corresponding second gain among a plurality of different second gains
according to each second section;

20 passing a high-frequency component of the non-linearly gamma-corrected digital
signal, to output a chrominance signal;

multiplying the chrominance signal by the corresponding selected second gain;

adding the result of the multiply to the luminance signal;

dividing the result of the add by approximately 2; and

25 clipping to 0 if the result of the division is less than 0, and clipping to the
maximum value of the gamma-corrected digital signal if the result of the division is
greater than the maximum value of the gamma-corrected digital signal, and outputting the
result.

25

20. The method of claim 19, wherein the plurality of second gains are approximately
proportional to the luminance level of the analog image signal.

IMAGING APPARATUS AND IMAGE SIGNAL PROCESSING METHOD PRODUCING WIDE DYNAMIC RANGE

5 **Abstract of the Disclosure**

An imaging apparatus and method provides a wide dynamic range and is capable of displaying an improved color in low-luminance regions, and an image signal processing method thereof. In the imaging apparatus, a first process divides the voltage level of an analog image signal into a plurality of sections and amplifies the analog image signal with a plurality of different gains according to each section, and a second process converts the analog image signal amplified with the different gains into a digital signal, and non-linearly gamma-corrects the digital signal according to each section. The plurality of different gains may be inversely proportional to the luminance level of the analog image signal. Thus, the level of an analog signal in a low-luminance region is amplified with a gain greater than in a high-luminance region before gamma correction, so that signal-to-noise (S/N) ratio in the low-luminance region of an image is increased, thereby improving sensitivity of the image following gamma correction. Accordingly, a wide dynamic range is provided. An optional chrominance controller of the imaging apparatus controls chrominance gain of the digital signal and outputs the result, thereby displaying an improved color in low-luminance regions of the image.

FIG. 1

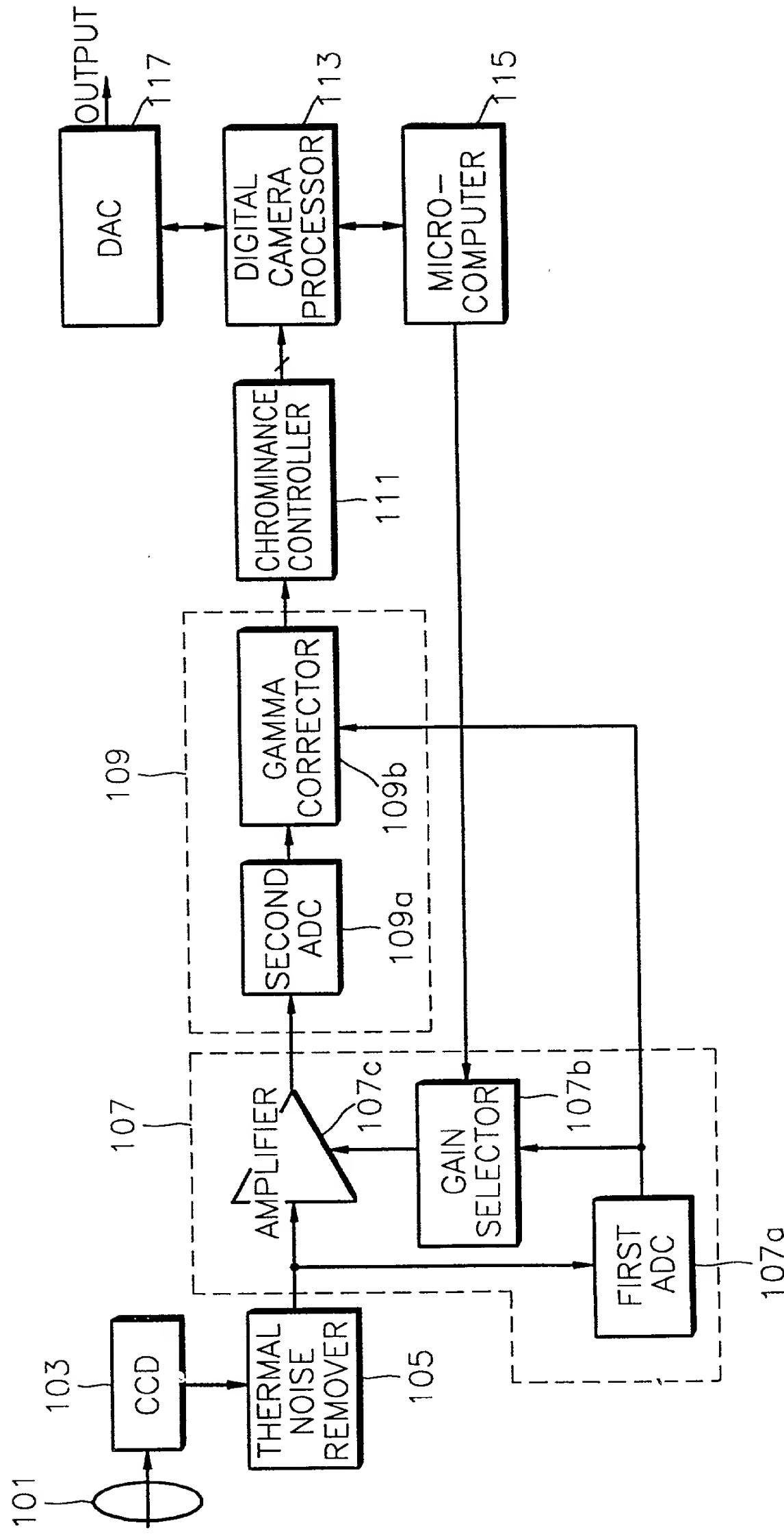


FIG. 2

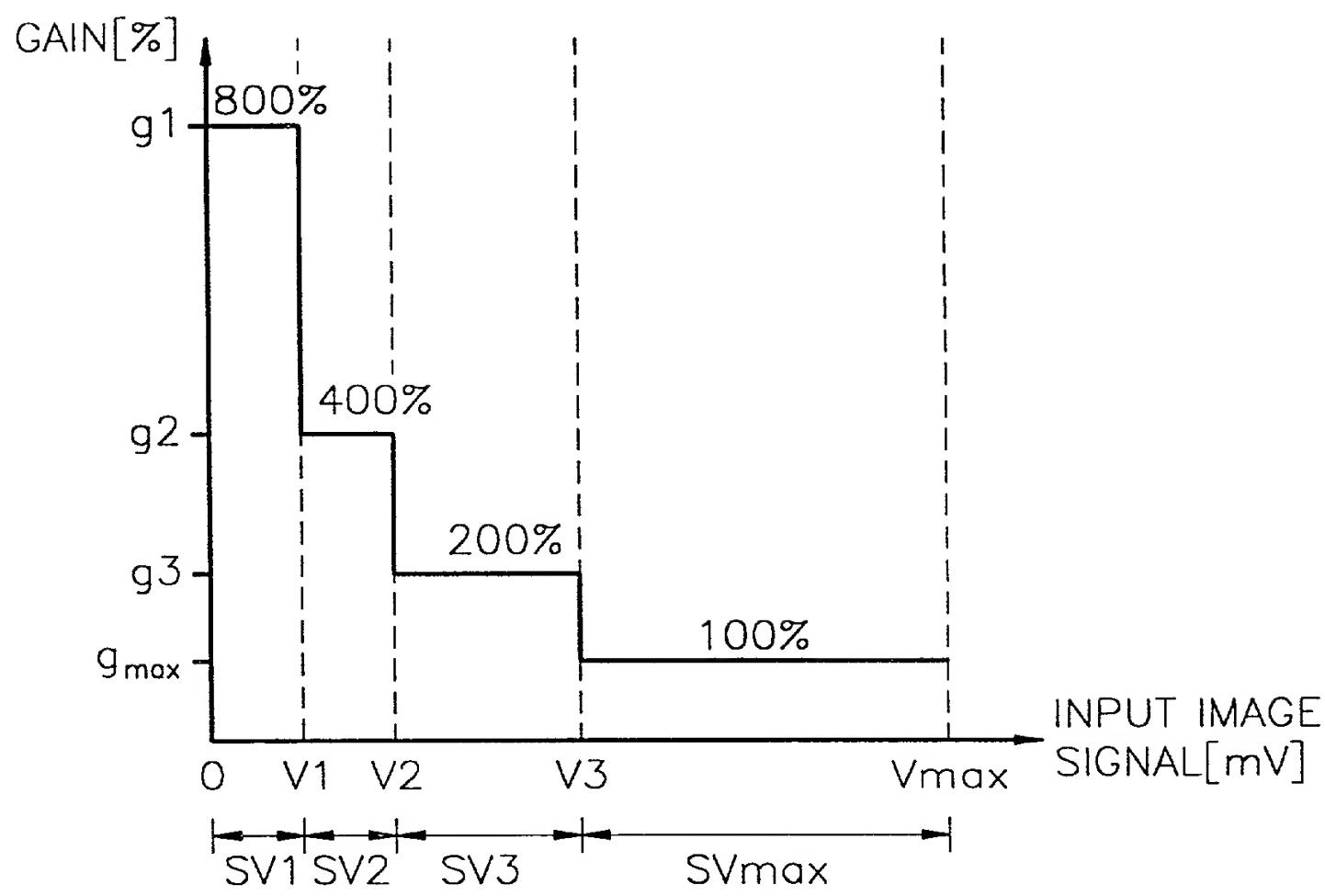


FIG. 3

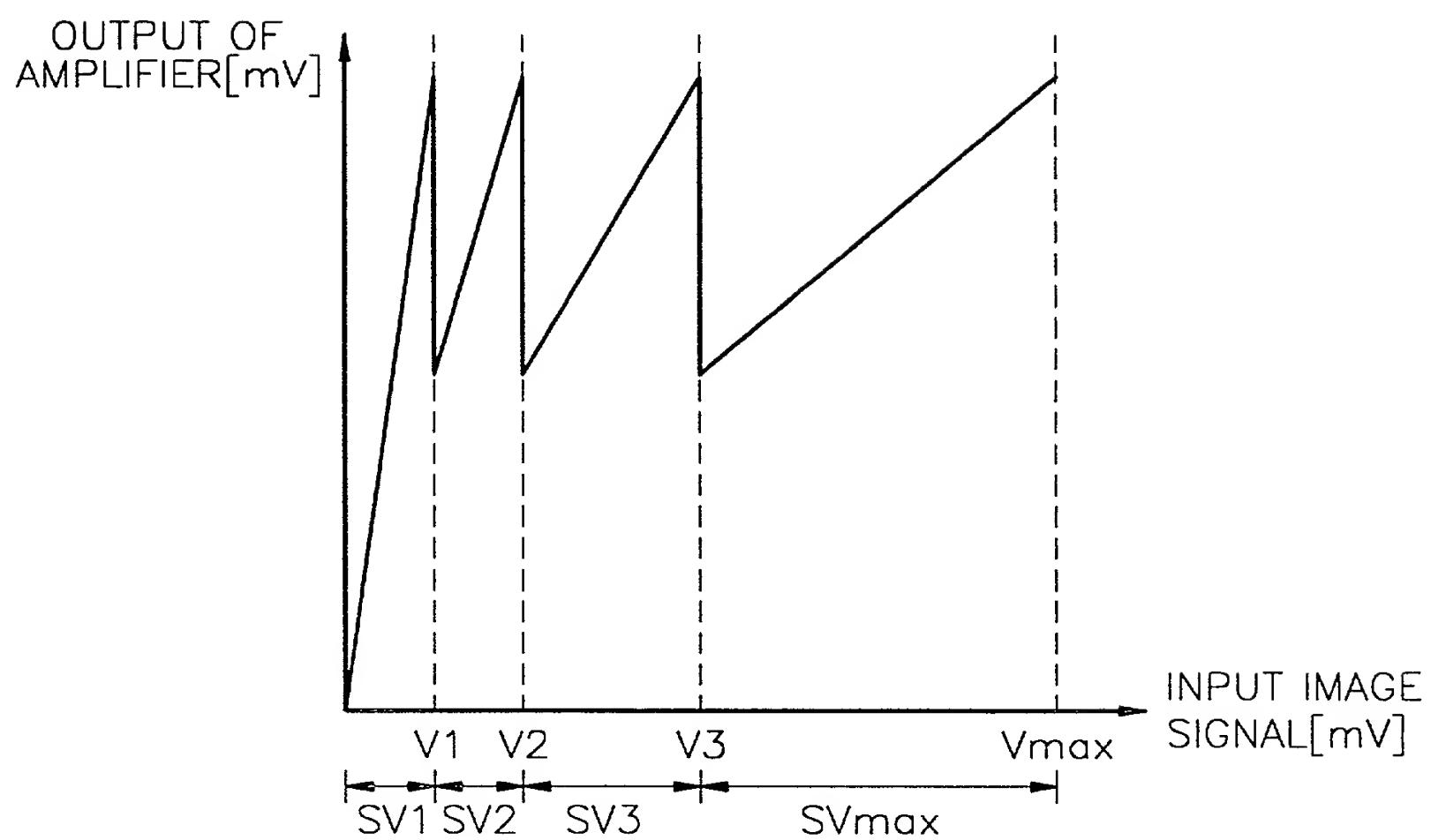


FIG. 4

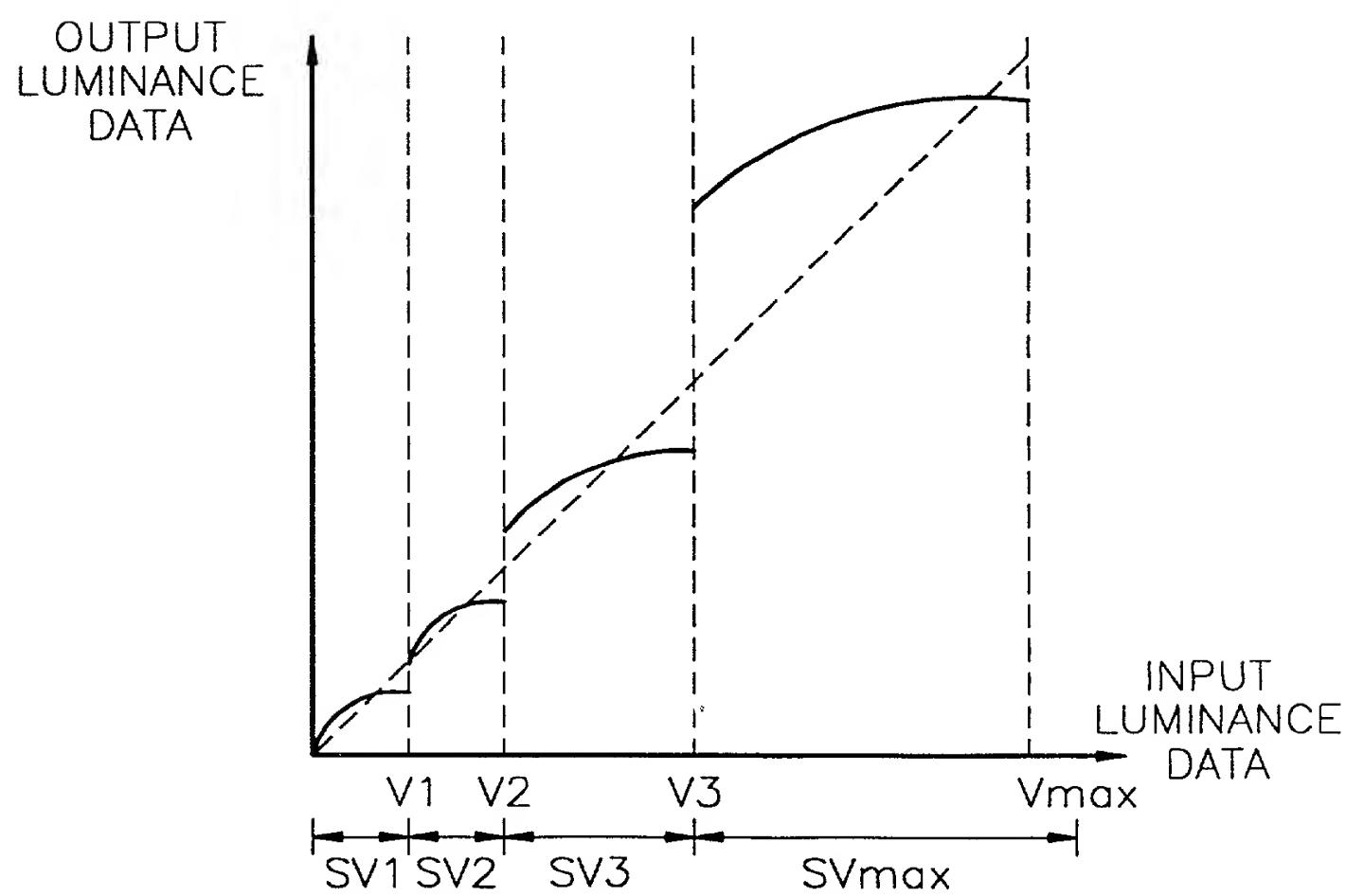


FIG. 5

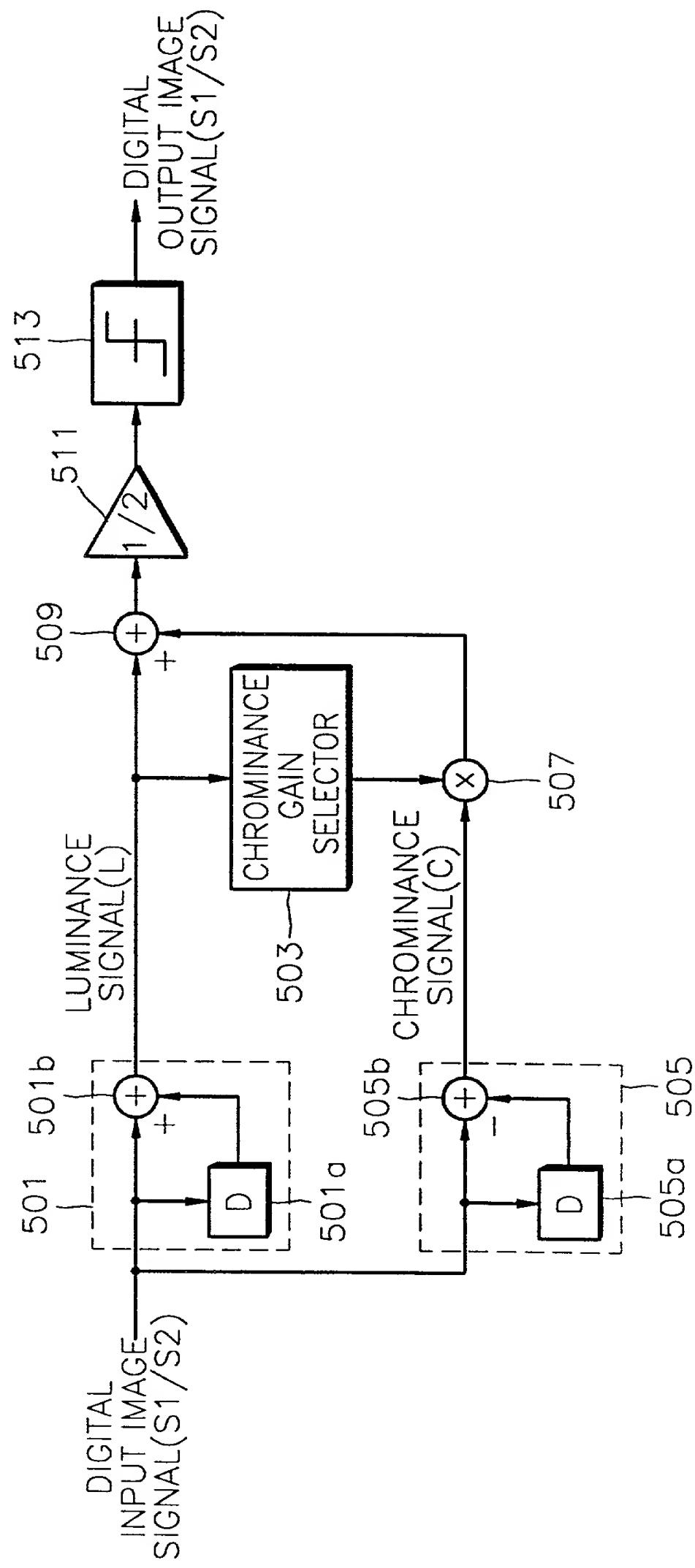
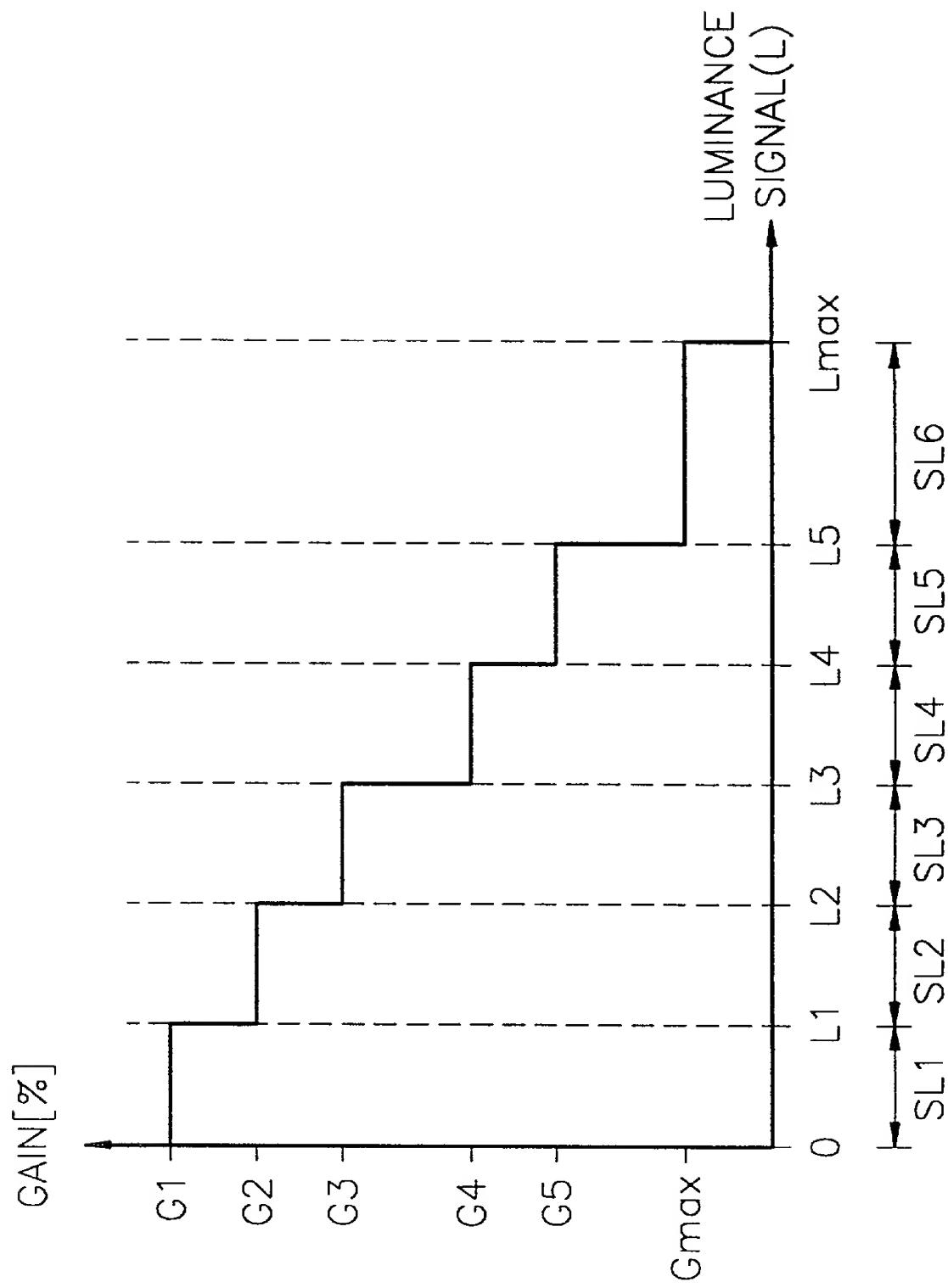


FIG. 6



**DECLARATION, PETITION AND POWER OF ATTORNEY FOR
PATENT APPLICATION**

Attorney Docket No:

SAMJ-069

JG612 09/27/98 PRO
03/23/95

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

**IMAGING APPARATUS AND IMAGE SIGNAL PROCESSING
METHOD PRODUCING WIDE DYNAMIC RANGE**

the specification of which (check only one):

is attached hereto.

was filed as United States Patent Application

Serial No. _____

on _____

and was amended

on _____

(if applicable)

was filed as PCT Patent Application

Serial No. _____

on _____

and was amended under PCT Article 19

on _____

(if applicable)

I hereby state that I have reviewed and understand the contents of the specification, including the claims as amended by any amendment referred to herein.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate or of any PCT international application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed:

PRIOR FOREIGN/PCT APPLICATION(S) AND ANY PRIORITY CLAIMS UNDER 35 U.S.C. § 119:

COUNTRY (if PCT indicate PCT)	APPLICATION NUMBER	DATE OF FILING (day, month, year)	PRIORITY CLAIMED UNDER 35 U.S.C. § 119 (YES/NO)
Republic of Korea	98-35852	1 September 1998	YES

DECLARATION, PETITION AND POWER OF ATTORNEY FOR PATENT APPLICATION

Attorney Docket No:
SAMJ-069

I hereby claim the benefit under Title 35, United States Code, §119(e) of any United States provisional application(s) listed below.

PRIOR U.S. APPLICATIONS FOR BENEFIT UNDER 35 U.S.C. § 119(e):

APPLICATION NUMBER	FILING DATE

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) or PCT international application(s) designating the United States of America that is/are listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in that/those prior application(s) in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56 which occurred between the filing date of the prior applications and the national or PCT international filing date of this application:

PRIOR U.S. APPLICATIONS OR PCT INTERNATIONAL APPLICATION(S) DESIGNATING THE U.S. FOR BENEFIT UNDER 35 U.S.C. § 120:

APPLICATION NUMBER (if PCT indicate PCT)	DATE OF FILING (day, month, year)	STATUS: (PATENTED, PENDING OR ABANDONED)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorneys and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

Mark G. Lappin	Reg. No. 26,618	Steven M. Mills	Reg. No. 36,610
Toby H. Kusmer	Reg. No. 26,418	David M. Mello	Reg. No. P43,799
Robert J. Schiller	Reg. No. 19,248	Anthony P. Onello	Reg. No. 38,572
Elizabeth A. Levy	Reg. No. 34,375	Ronald R. Demsher	Reg. No. 42,478
David Silverstein	Reg. No. 26,336	Carolyn G. d'Agincourt	Reg. No. 43,327

Send Correspondence to:

Anthony P. Onello, Jr., Esq.
LAPPIN & KUSMER
200 State Street
Boston, Massachusetts 02109

Direct Telephone Calls to:

Anthony P. Onello, Jr.
(617) 330-1300
(617) 330-1311 (facsimile)

Wherefore I petition that letters patent be granted to me for the invention or discovery described and claimed in the attached specification and claims, and hereby subscribe my name to said specification and claims and to the foregoing declaration, power of attorney, and this petition.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Signature

Date

March 12, 1999

Full Name of 1st Inventor	Family Name Hwang	First Given Name Jung-Hyun	Second Given Name
Residence & Citizenship	City Sungnam-city, Kyungki-do	State or Foreign Country Republic of Korea	Country of Citizenship Republic of Korea
Post Office Address	Post Office Address 610-203, Woosung Apt., Jungdun Maeul, Jungja-dong, Bundang-gu	City Sungnam-city, Kyungki-do	State & Zip Code/Country Republic of Korea

DECLARATION, PETITION AND POWER OF ATTORNEY FOR PATENT
APPLICATION

Attorney Docket No:
SAMJ-069

Signature		Date	
<i>Woon Na</i>		March 12, 1999	
Full Name of 2nd Inventor	Family Name Na	First Given Name Woon	Second Given Name
Residence & Citizenship	City Seoul	State or Foreign Country Republic of Korea	Country of Citizenship Republic of Korea
Post Office Address	Post Office Address 10-478, Shinlimbon-dong Kwanak-gu	City Seoul	State & Zip Code/Country Republic of Korea